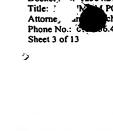
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Inventor: HUI et al.

Docket No.: 12364.27USU1

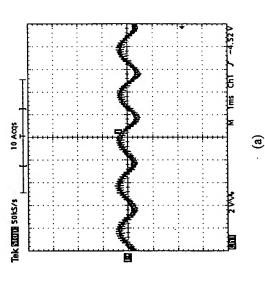
Title: 'POWER TRACKING TECHNIQUE FOR SOLAR PAN
Attorne, an inventor of the chael D. Schumann (Reg. No. 30,422)

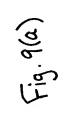


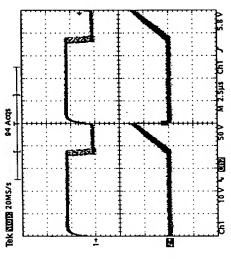
30 Acqs Ch3 10.0 V € (11) 10.0mV Tek SIGIE 1.00MS/s

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Fig. 8 Experimental waveforms of the SEPIC converter. Ch2: switch voltage stress, 50V/div; Ch3: input voltage, 10V/div; Ch4: input current, 0.5A/div.







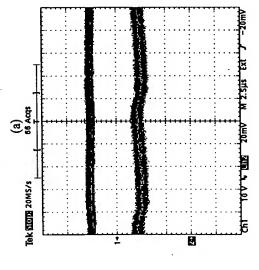


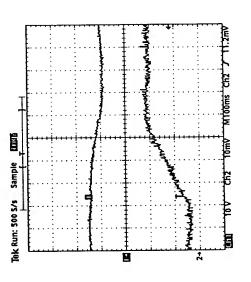
Fig. 7 Detailed experimental waveforms of the SEPIC converter. (a) Ch1: gate signal, 10V/div; Ch2: switch voltage stress, 50V/div. (b) Ch1: input voltage, 10V/div; Ch2: input current, 0.5A/div.

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from 500W to 900W. Ch1: input voltage, 10V/div. Ch2: input current, 0.5A/div. Fig. 10 Transient waveforms of the SEPIC converter subject to P_{lamp} changed

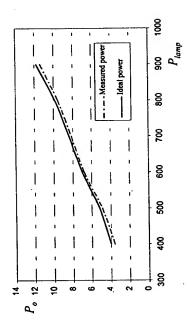
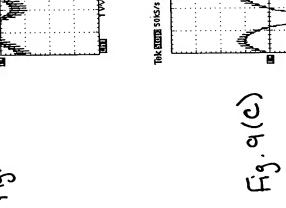


Fig. 11 Comparison of maximum solar panel output power using proposed method and the ideal ones in Fig. 6(b), under different Plamp.

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Fig. 9 Waveform of $\delta \widetilde{v}_i$ with respect to different value of \mathfrak{R} . (a) $\mathfrak{R}=0.02$. (b) \mathfrak{R} = 0.05. (c) $\Re = 0.1$.